

## In the Specification

*In the specification, please amend paragraph [0034] as follows:*

[0034] In the preferred embodiment, the ATE compiler is programmed to configure the internal signal switches to force all drive edges and waveforms to occur on one channel and all compare waveforms to occur on an alternate channel. The data to be applied, in a vector file, identifies the appropriate tester cycle type and the drive or compare data. Fig. 5 depicts the dual mode configuration of the present invention. A first channel of electronics 50, channel A, comprises a channel driver 52 having an input 54 for drive data, a drive inhibit port 56 for drive timing edges, and an output 58 which sends a test signal to the DUT (not shown) through pin electronics 60, a device interface board wire 62, and a device socket 64. The output line 58 may also be electrically connected to a channel receiver 66 and input port 68; however, in the preferred embodiment, the channel receiver 66 is not activated. A dual driver-receiver pair, which is a second channel of electronics, referred to as the channel B electronics 80, interfaces with the output 58 of the channel A electronics through a signal line 82 that connects on the same printed circuit board as the channel A electronics, and is preferably made to be as short in length as possible, and should be made as close as possible to the DUT. In the preferred embodiment, the channel driver circuit 84 of the channel B electronics is not activated. Instead, channel receiver electronics 88 receives input data from signal line 82 through input port 86. A second input port 90 of the channel receiver electronics 88 receives compare data. The channel receiver electronics 88 is timed

by compare data timing edges through a clock gate port 92. The time delays associated with signal transmission through the pin electronics, device interface board, and device socket,  $T_{pe}$ ,  $T_{dib}$ , and  $T_s$  respectively, are indicated for the signal lines. It is assumed that the delay time through the pin electronics,  $T_{pe}$ , is equivalent for both channels.